

**REMARKS**

The application has been editorially revised. Favorable action on the application is solicited.

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Respectfully submitted,

By 

Mark J. Thronson

Registration No.: 33,082

DICKSTEIN SHAPIRO MORIN &

OSHINSKY LLP

2101 L Street NW

Washington, DC 20037-1526

(202) 785-9700

Attorneys for Applicant

05902170-071101

MARK-UP VERSION SHOWING CHANGES MADE

[Fig. 1 illustrates] Figs. 1(a) through 1(e) illustrate a thin film transistor and the manufacturing process thereof according to a first embodiment;

[Fig. 3 illustrates] Figs. 3(a) through 3(e) illustrate a thin film transistor and the manufacturing process thereof according to a second embodiment;

[Fig. 5 illustrates] Figs. 5(a) through 5(f) illustrate a thin film transistor and the manufacturing process thereof according to a third embodiment; and

[Fig. 6 illustrates] Figs. 6(a) through 6(d) illustrate a conventional channel etched type thin film transistor and the manufacturing steps thereof.